

### **REMARKS**

Claims 1-14 and 16-25 are pending in the application. Claims 1, 9, 14, 18, 22 and 24 have been amended.

Applicant believes that this amendment addresses the Examiner's rejection and that any changes do not introduce new matter into the specification, limit the scope of the claims or result in any prosecution history estoppel.

#### **Claim Rejections – 35 USC S.103**

The Examiner rejected claims 1-14 and 16-25 under 35 U.S.C. 103 as being unpatentable over Takahara (U.S. Patent 5,436,635) in view of Shields (US Patent 4,870,396). Applicant respectfully disagrees with the Examiner's rejection. Both Takahara and Shields, alone and in combination, fail to teach or suggest a second circuit configuration to alternatively sample the respective voltage signals of the respective voltage signal storage elements at a substantially predetermined rate, wherein data is updated in accordance with the characteristics of the storage elements.

In particular, embodiments of the invention provide logically inverted storage capacitors under each light element. Both positive and negative data values are simultaneously driven onto the storage capacitors according to whatever process is creating the pixels for example, 3D game, video playback etc. In turn, and completely asynchronous to the data-write process, the two capacitors are alternately and continuously sampled at the AC rate required by the liquid crystal material. *Data on the storage capacitors is updated according to the leakiness of the storage capacitors only, and not according to the requirements of the liquid crystal material.* For example, pixels are updated according to when they change, not according to AC requirements typical of liquid crystal display systems or phosphor decay of CRT display systems. Depending on the design, these capacitors may hold their data to sufficient precision

for a certain time or more. This results in a completely asynchronous, randomly addressable display requiring low refresh. Nowhere does Takahara or Shields, alone or in combination teach the above.

### CONCLUSION


In view of the foregoing, it is respectfully asserted that all of the claims pending in this patent application are in condition for allowance.

The required fee for a two month extension of time is enclosed. No additional fees are required for additional claims. Should it be determined that an additional fee is due under 37 CFR §§1.16 or 1.17, or any excess fee has been received, please charge that fee or credit the amount of overcharge to deposit account #02-2666.

If the Examiner has any questions, he is invited to contact the undersigned at (323) 654-8218. Reconsideration of this patent application and early allowance of all the claims is respectfully requested.

Respectfully submitted,

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### CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to: Mail Stop AF, Commissioner for Patents, Post Office Box 1450, Alexandria, Virginia 22313-1450 on February 3, 2005.

  
Margaux Rodriguez February 3, 2005